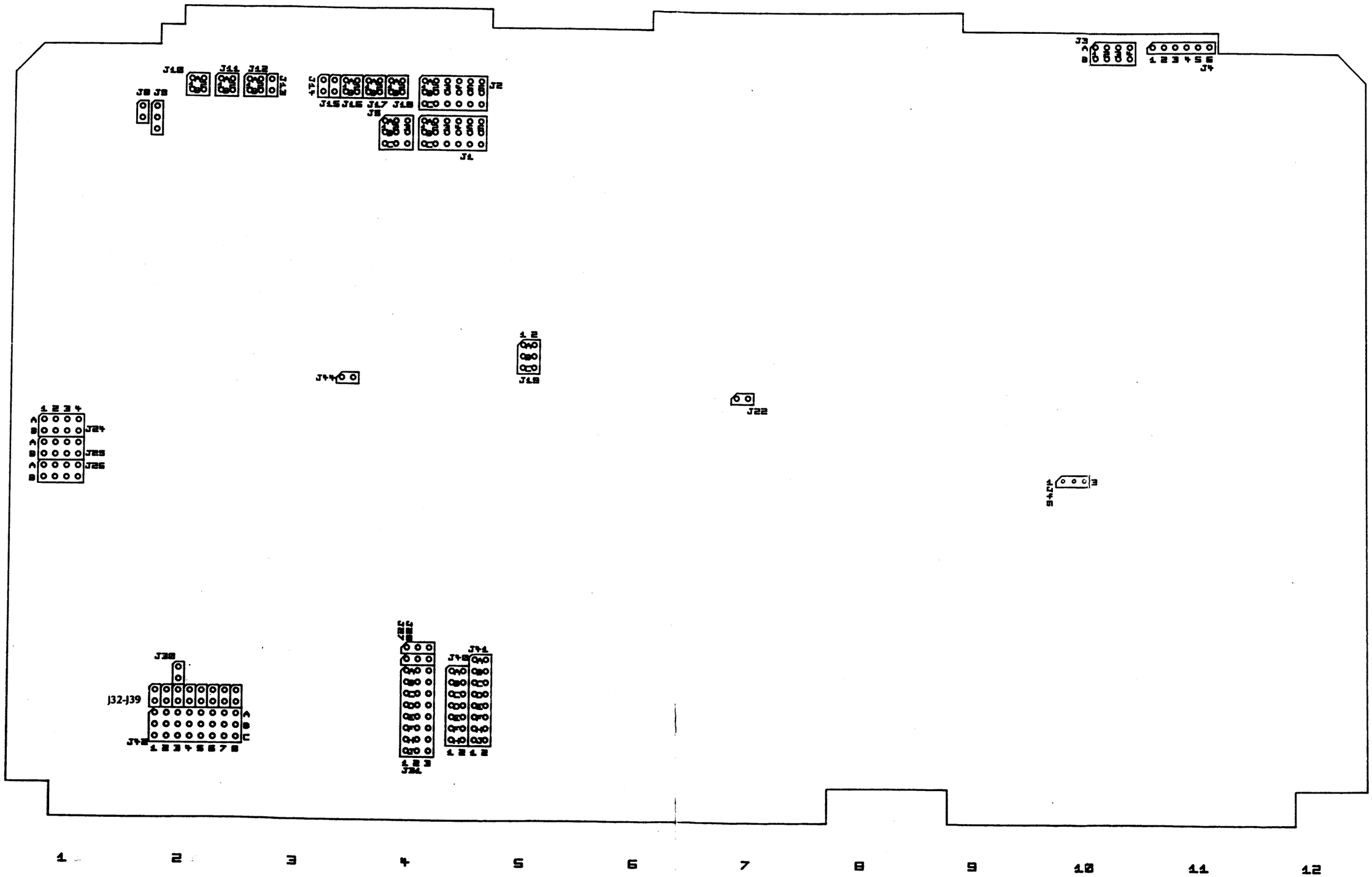
MPU-28 REV E
JUMPER WORKSHEEET

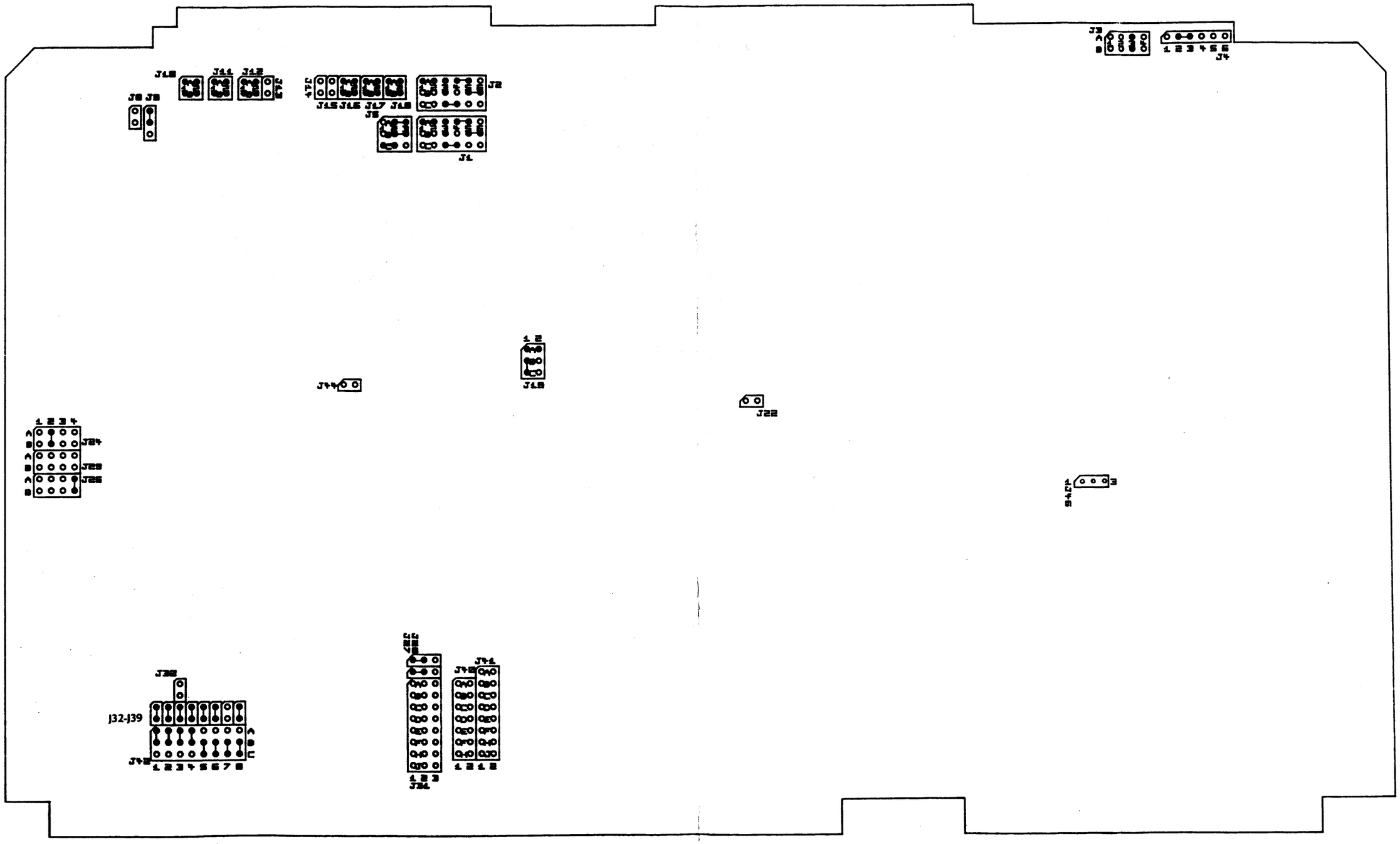
P-311/epc/1-10-90



MPU-28 REV E

P-310/epc/1-10-90

STANDARD JUMPER CONFIGURATION



STANDARD JUMPER CONFIGURATIONS

BUS CLOCK (Sec. 4-4)

J34
J37

BUS ERROR TIMEOUT (Sec. 3-7)

J30

BUS PRIORITY & RELEASE (Secs. 4-1 to 4-3)

J35
J36

J39

CACHE MEMORY (Sec. 3-12)

J22

DYNAMIC RAM SIZE (Sec. 3-2)

J46
1 2 3

EPROM COMPONENTS (Sec. 3-1)

J1
J2

J3
J5

J24

SERIAL I/O (Sec. 3-8)

J8
J10

J11
J12

J13
J14

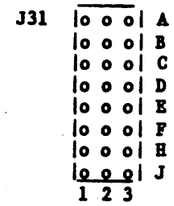
J15
J16

J17
J18

INTERRUPTS (Sec. 3-4)

J9

J32

J31 
1 2 3

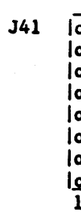
iSBX MODULE (Sec. 3-9)

J44

MAILBOX ADDRESS (Sec. 3-5)

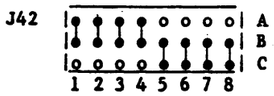
J27
1 2 3

J40
1 2

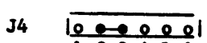
J41 
1 2

MULTIBUS ADDRESS OF THE MPU-28 (Secs. 4-6, 4-7)

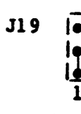
J28
1 2 3

J42 
1 2 3 4 5 6 7 8

RESET & NMI (Sec. 7-6)

J4 
1 2 3 4 5 6

RESET OPTIONS (Sec. 4-8)

J19 
1 2

J33

J38

MPU-28 QUICK REFERENCE SHEET

This quick reference sheet lists all of the jumper areas and connectors on the MPU-28. The jumper chart below explains where each J area is discussed in the manual, what its general function is, and what function it is jumpered for when shipped to you. To the left are illustrations of the standard jumper settings by category. Use this sheet with the following jumpering worksheet to configure your board. To find a jumper area on the jumpering worksheet, look up its location using the grid area column in the jumper chart.

MPU-28 JUMPER CHART

J #	Grid Area	Standard Jumper Setting	General Function	J #	Section
J1	A-7	16K x 8 EPROMs	U48 memory device pinout control	J1	3-1-2
J2	A-8	16K x 8 EPROMs	U18 memory device pinout control	J2	3-1-2
J3	A-10	16K x 8	EPROM size	J3	3-1-1
J4	A-11	NMI and reset are inactive	Reset/NMI connector	J4	4-8; 7-6
J5	A-4	16K x 8 EPROM size	U19 & U49 pinout control	J5	3-1-1
J6	N/A	N/A	RS-232-C dual serial port header	J6	3-8; 7-2
J7	N/A	N/A	Parallel port header	J7	3-10-2; 7-3
J8	A-1	No TXC enabled for channels B & A	Channel B RS-232-C TXC receive	J8	3-8-2
J9	A-1	Interrupt asserted/negated by MPU-28	GENIRQ selection	J9	3-4-1
J10	A-2	Channels B & A talk to DTE	Channel B RS-232-C TXD/RXD	J10	3-8-1
J11	A-2	Channels B & A talk to DTE	Channel B RS-232-C CTS/RTS	J11	3-8-1
J12	A-3	Channels B & A talk to DTE	Channel B RS-232-C DTR/DCD	J12	3-8-1
J13	A-3	No TXC enabled for channels B & A	Channel B RS-232-C TXC transmit	J13	3-8-2
J14	A-3	No TXC enabled for channels B & A	Channel A RS-232-C TXC receive	J14	3-8-2
J15	A-3	No TXC enabled for channels B & A	Channel A RS-232-C TXC transmit	J15	3-8-2
J16	A-3	Channels B & A talk to DTE	Channel A RS-232-C TXD/RXD	J16	3-8-1
J17	A-3	Channels B & A talk to DTE	Channel A RS-232-C CTS/RTS	J17	3-8-1
J18	A-4	Channels B & A talk to DTE	Channel A RS-232-C DTR/DCD	J18	3-8-1
J19	C-5	MPU-28 drives INIT during reset	Multibus reset options	J19	4-8-1
J20	N/A	N/A	iSBX connector	J20	3-9; 7-4
J22	C-7	Cache can be enabled	Cache enable/disable	J22	3-12
J24	D-1	4 wait states	EPROM access time	J24	3-1-4
J25	D-1	Timeout period disabled	Watchdog timeout select (long)	J25	3-6
J26	D-1	0.10 seconds	" " " (short)	J26	3-6
J27	F-4	Mailbox address is 16 bits	Mailbox address: 8- or 16-bit	J27	3-5-1
J28	F-4	Byte swapping is controlled by address of access	Byte swap control: A26 or status bit	J28	4-6
J30	F-2	Bus error timeout is enabled	Bus error timeout enable/disable	J30	3-7
J31	F-4	MPU-28 won't receive or generate Multibus interrupts	Interrupt level assignments	J31	3-4-1
J32	F-2	Received Multibus interrupt enabled	Received-Multibus-Interrupt enable	J32	3-4-1
J33	F-2	Drive INIT on the Multibus	Drive INIT on the Multibus	J33	4-8-1
J34	F-2	MPU-28 is primary master; generates BCLK	Drive bus clock on the Multibus	J34	4-4
J35	F-2	MPU-28 is set up for serial priority	Bus priority out control	J35	4-2
J36	F-2	BPRN* is received from the Multibus	Receive bus priority in control	J36	4-2
J37	F-2	MPU-28 is primary master; generates CCLK	Drive common clock on the Multibus	J37	4-4
J38	F-2	INIT is not received from the Multibus	Receive INIT from the Multibus	J38	4-8-1
J39	F-2	MPU-28 is set up for serial priority	Bus request (CBRQ) options	J39	4-2; 4-3
J40	F-4	$000	Mailbox: Multibus I/O address (upper)	J40	3-5-1
J41	F-4	$00	Mailbox: Multibus I/O address (lower)	J41	3-5-1
J42	G-2	$00	Multibus access address to MPU-28 mem.	J42	4-7-2
J44	D-3	Standard iSBX module; A4 not supplied	A4 supplied for nonstandard iSBX modules	J44	3-9
J46	E-10	1 Mbit DRAM chips	Memory: 256kbit or 1Mbit DRAM chips	J46	3-2